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04/29/2010

## **DUAL-ADDRESSED RECTIFIER STORAGE DEVICE**

### **CROSS-REFERENCE TO RELATED APPLICATION**

This application is a continuation in part of Application Ser. No. 08/610,992 entitled "Dual Addressed Rectifier Storage Device", filed Mar. 5, 1996, now U.S. Pat. No. 5,673,218, Sep 30, 1997.

### **BACKGROUND OF THE INVENTION**

#### **1. Field of the Invention**

The present invention relates to electronic data retrieval devices, and more particularly to electronic digital logic devices having semiconductor mass storage capabilities by virtue of their data being stored in highly symmetrical arrays of diodes.

#### **2. Prior Art**

Most present day devices having mass storage capabilities rely on such moveable media as magnetic disks, optical compact disks, digital tape or the like. Some devices having large storage [Capabilities]capabilities have utilized large numbers of read-only memory (ROM) devices.

Many read-only memory (ROM) devices have been disclosed having a wide variety of implementations. In many of these devices the bit storage means is accomplished through the application of gates or transistors. But, a subset of these ROM devices has accomplished the bit[,] storage means through the use of a matrix of diodes, as was disclosed by Robb in U.S. Pat. No. 3,245,051. Many of these ROM devices[ is] include diode matrix storage means utilizing a